

L Number	Hits	Search Text	DB	Time stamp
1	615	(two second)near3 (address adj bus\$3)with(processor micro\$lprocessor controller micro\$lcontroller)	USPAT; US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/13 11:04
2	701	(two second)near3 (address adj bus\$3)with(processor micro\$lprocessor controller micro\$lcontroller cpu)	USPAT; US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/13 12:16
3	37	(two second)near3 (address adj bus\$3)with(processor micro\$lprocessor controller micro\$lcontroller cpu)with (separate different distinct)	USPAT; US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/13 13:00
4	200	(separate different distinct)near3 (address adj bus\$3)with(processor micro\$lprocessor controller micro\$lcontroller cpu)	USPAT; US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/13 13:00
5	180	((separate different distinct)near3 (address adj bus\$3)with(processor micro\$lprocessor controller micro\$lcontroller cpu)) not ((two second)near3 (address adj bus\$3)with(processor micro\$lprocessor controller micro\$lcontroller cpu)with (separate different distinct))	USPAT; US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/13 16:07
6	17	("3906457" "4818932" "4847758" "4961172" "5001671" "5226010" "5386579" "5404464" "5606714" "5652847" "5655142" "5664140" "5680567" "5682498" "5813038" "6026466" "6209049").PN.	USPAT	2004/01/13 14:11
7	3	710/52,53,107.ccls. and ((separate different distinct)near3 (address adj bus\$3)with(processor micro\$lprocessor controller micro\$lcontroller cpu))	USPAT; US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/13 16:10
8	21	710/52,53,107.ccls. and ((two second)near3 (address adj bus\$3)with(processor micro\$lprocessor controller micro\$lcontroller cpu))	USPAT; US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/13 16:10
-	0	("busadj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4) and (interfac\$3 same bus) and RISC) and dsl	USPAT; US-PGPUB	2003/08/07 15:02
-	2521	bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)	USPAT; US-PGPUB	2003/08/07 15:05
-	1585	(bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)) and (interfac\$3 same bus)	USPAT; US-PGPUB	2003/08/07 15:07
-	248	((bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)) and (interfac\$3 same bus)) and RISC	USPAT; US-PGPUB	2003/08/07 15:07
-	11	((bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)) and (interfac\$3 same bus)) and RISC) and dsl	USPAT; US-PGPUB	2003/08/07 15:09
-	11	((bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)) and (interfac\$3 same bus)) and RISC) and ?dsl	USPAT; US-PGPUB	2003/08/14 14:59
-	185	((bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)) and (interfac\$3 same bus)) and command	USPAT; US-PGPUB	2003/08/07 15:10
-	0	((bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)) and (interfac\$3 same bus)) and RISC) and command) and PCM	USPAT; US-PGPUB	2003/08/07 15:10
-	13	((bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)) and (interfac\$3 same bus)) and RISC) and PCM	USPAT; US-PGPUB	2003/08/07 15:23

-	193	toroid\$3 with memory	USPAT;	2003/08/11
-	1	(toroid\$3 with memory) and (PCM or (pulse adj code adj modulat\$3))	US-PGPUB	11:34
-	32	(toroid\$3 with memory) and fram\$3	USPAT;	2003/08/11
-	1	6341335.pn.	US-PGPUB	11:33
-	4625	(first adj bus)and (second adj bus)	USPAT;	2003/08/11
-	162	((first adj bus)and (second adj bus)) and (PCM or (pulse adj code adj modulat\$3))	US-PGPUB	13:52
-	11	((first adj bus)and (second adj bus)) and (PCM or (pulse adj code adj modulat\$3))) and Dsl	USPAT;	2003/08/11
-	106	((first adj bus)and (second adj bus)) and (PCM or (pulse adj code adj modulat\$3))) and compar\$5	US-PGPUB	13:53
-	101	((first adj bus)and (second adj bus)) and (PCM or (pulse adj code adj modulat\$3))) and compar\$5) and (port or buffer)	USPAT;	2003/08/11
-	58	data adj memory adj address adj bus	US-PGPUB	14:48
-	792	advantage same (bus with (separat\$3 or split or divide))	USPAT;	2003/08/11
-	62	(advantage same (bus with (separat\$3 or split or divide))) and (address adj bus) same advantage	US-PGPUB	14:49
-	34764	(input adj (port or interface)) and (output adj (port or interface))	USPAT;	2003/08/11
-	814	((input adj (port or interface)) and (output adj (port or interface))) and frame adj (detect\$3 or loca\$7 of find\$3 or identif\$7 or determin\$3)	US-PGPUB	14:50
-	623	((input adj (port or interface)) and (output adj (port or interface))) and frame adj (detect\$3 or loca\$7 of find\$3 or identif\$7 or determin\$3)) and (frame same input)	USPAT;	2003/08/11
-	43	((input adj (port or interface)) and (output adj (port or interface))) and frame adj (detect\$3 or loca\$7 of find\$3 or identif\$7 or determin\$3)) and (frame same input) and (output same (PCM or (pulse adj code adj modulat\$3)))	US-PGPUB	2003/08/11
-	3471	output with PCM	USPAT;	2003/08/11
-	271	(output with PCM) and ((separate or distinct or different) with register)	US-PGPUB	15:22
-	133	((output with PCM) and ((separate or distinct or different) with register)) and (frame with (detect\$3 or loca\$7 of find\$3 or identif\$7 or determin\$3))	USPAT;	2003/08/11
-	8	((output with PCM) and ((separate or distinct or different) with register)) and (frame with (detect\$3 or loca\$7 of find\$3 or identif\$7 or determin\$3))) and (data adj pattern)	US-PGPUB	2003/08/11
-	27	((output with PCM) and ((separate or distinct or different) with register)) and (frame with (detect\$3 or loca\$7 of find\$3 or identif\$7 or determin\$3))) and (data adj (pattern or sequence or arrangement))	USPAT;	2003/08/11
-	19	(input adj (buffer or port)) same xDSL	US-PGPUB	2003/08/11
-	130	(input adj (buffer or port)) and xDSL	USPAT;	2003/08/11
-	1	5400369.pn.	US-PGPUB	2003/08/11

-	15	710/52.ccls. and ((bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)) and (interfac\$3 same bus))	USPAT; US-PGPUB	2003/08/14 15:03
-	15	710/52.ccls. and (bus adj (separat\$3 or isolat\$3 or segregat\$3 or divi\$4)) and (interfac\$3 same bus)	USPAT; US-PGPUB	2003/08/14 15:02
-	13	DSL and (frame adj align\$3)	USPAT; US-PGPUB	2003/08/14 14:58
-	0	DSL adj frame adj align\$3	USPAT; US-PGPUB	2003/08/14 13:04
-	177	micro\$1processor near3 two near5 address	USPAT; US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/12 16:59
-	32	(micro\$1processor near3 two near5 address) and (address adj bus)with two	USPAT; US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/12 17:07
-	53	(processor micro\$1processor controller micro\$1controller)near5 (two adj address)with bus\$3	USPAT; US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/12 17:31
-	158	(processor micro\$1processor controller micro\$1controller)near5 (second adj address)with bus\$3	USPAT; US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/12 17:32
-	158	((processor micro\$1processor controller micro\$1controller)near5 (second adj address)with bus\$3) not ((micro\$1processor near3 two near5 address) and (address adj bus)with two)	USPAT; US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/12 17:32
-	157	((processor micro\$1processor controller micro\$1controller)near5 (second adj address)with bus\$3) not ((processor micro\$1processor controller micro\$1controller)near5 (two adj address)with bus\$3)	USPAT; US-PGPUB; EPO; DERWENT; IBM_TDB	2004/01/12 17:32